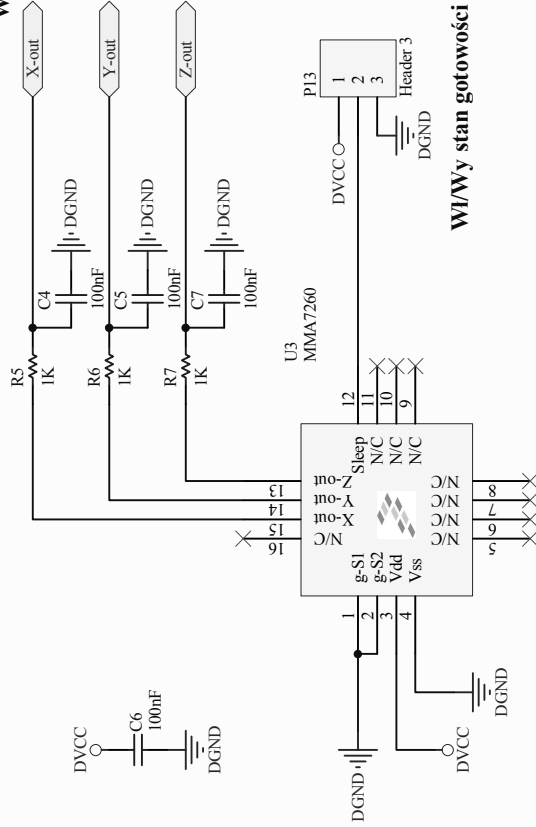


Wyjście XYZ do procesora



Wi/Wy stan gotowości

Title			
Size	Number	Revision	
A4	Mariusz Piotr Lasota		
Date:	2008-08-26	Sheet	of
File:	D:\#projekt\...MMA7260.SchDoc	Drawn By:	